219.39069X00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Shmuel WIMER

Serial No.:   09/893,527

Filed: 29 June 2001

For: ARRANGEMENTS FOR AUTOMATIC
             RE-LEGGING OF TRANSISTORS

Art Unit: 2123

Examiner: Unassigned

PRELIMINARY AMENDMENT

Honorable Commissioner for Patents
Washington, D.C. 20231   6 September 2001

Sir:

      Prior to examination of the above-identified application on the merits,

Applicant respectfully submits the following for entry and consideration.

IN THE SPECIFICATION:

      Page 4, line 11 through page 5, line 2, delete the paragraph in its entirety.

      Please enter the following replacement paragraph for the paragraph spanning

pages 5 and 6:

      --FIG. 1 illustrates an example migration arrangement 100 (including an

example compactor) in simplistic form, such FIG. being useful in explanation and

understanding both background and example embodiments of the present invention.

More specifically, shown is a source layout 110 fed to a compactor 120. The source

layout 110 can be of any type of suitable code, for example, a non-exhaustive listing

including Verilog or VHDL code. The compactor 120 further takes into consideration

target process design rules 130 as well as user-specified constraints 140, and